



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of) Group Art Unit: 2811
Shunpei YAMAZAKI et al.)
Serial No.: 09/334,646)
Filed: June 17, 1999)
For: SEMICONDUCTOR DEVICE)
AND FABRICATION)
METHOD THEREOF)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents and Trademarks, Washington, D.C. 20231, on 5-30-2000.



AMENDMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

In response to the Office Action mailed December 27, 2000, please amend the above identified application as follows.

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IN THE CLAIMS:

Please cancel claims 15 and 21 without prejudice or disclaimer, amend claims 1-10 and 38-57 and add new claims 58-70 as follows:

1.(Amended) An electro-optical device comprising:

at least two transistors provided on an insulating surface in a XY-branching circuit of a peripheral circuit of said electro-optical device;

a common gate wiring provided on said insulating surface and connected with said two transistors at gate electrodes of said two transistors;

a common source wiring provided on said insulating surface and connected with said two transistors at one of source and drain of each of said two transistors; and

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